Print Form

Experiment 7
Op-Amp Circuits II
In-Lab Report

Inverting Adder

Question 1:
Describe the signal that you placed on the input and the result you got.

Question 2:
Draw the non-inverting adder you have designed.

Integrator

Question 1:

f (Hz) at input ***f (Hz) at input***	***f (Hz) at output***	***v_O (peak-to-peak in V)***
1000	990 Hz	2.60 V
4000	4040	0.616 V
7000	7.030	0.368 V
10000	9	0.232V

Table B-1

Question 2:

At what frequency does the output stop corresponding to the integral of the input?

At 2.25 KH, the output stops corresponding to the integral of the input.

Question 3:

Find out what causes the deterioration in the integration action of the op-amp.

as the frequency increases, the integration action of the op-amp starts making errors since were getting out of the bandwidth. in addition of that, at high frequency, the capacitor turns to a short circuit which ruins the integrator op-amp ciruit.

Differentiator

Question 1:

f (Hz) at input ***f (Hz) at input***	***f (Hz) at output***	***v_O (peak-to-peak in V)***
100	100.3 Hz	0.036 V
400	400.1 Hz	0.126 V
700	700.12 Hz	0.232 V
1000	1020 Hz	0.320

Table C-1